

IN THE SPECIFICATION

Please amend the paragraph beginning at page 13, line 12 as follows:

5       The LFU/MFU circuitry 225 comprises maximum (max.) value detectors  
433, 432, and 431, a data register 440, multiplexers (MUXs) 455, 460, and 445, an LFU  
detector 465, selection circuitry 450, and one-bit adders 446 and 470. An LFU/MFU  
calculator is comprised of the MUX 455 and LFU detector 465. The LFU/MFU  
calculator 485 is an example of a device used to perform either a LFU calculation or an  
MFU calculation. The data register 440 comprises non-inverted versions 441, ~~443~~ 444 of  
10 the LUse counters 420-1 and 420-2 and inverted versions 442, 443 of the LUse counters  
420-1, 420-2. The data register 440 is adapted to invert the data from the LUse counters  
420-1 and 420-2. The data register 440 is also adapted to shift the data from the LUse  
counters 420-1, 420-2, for reasons described below.